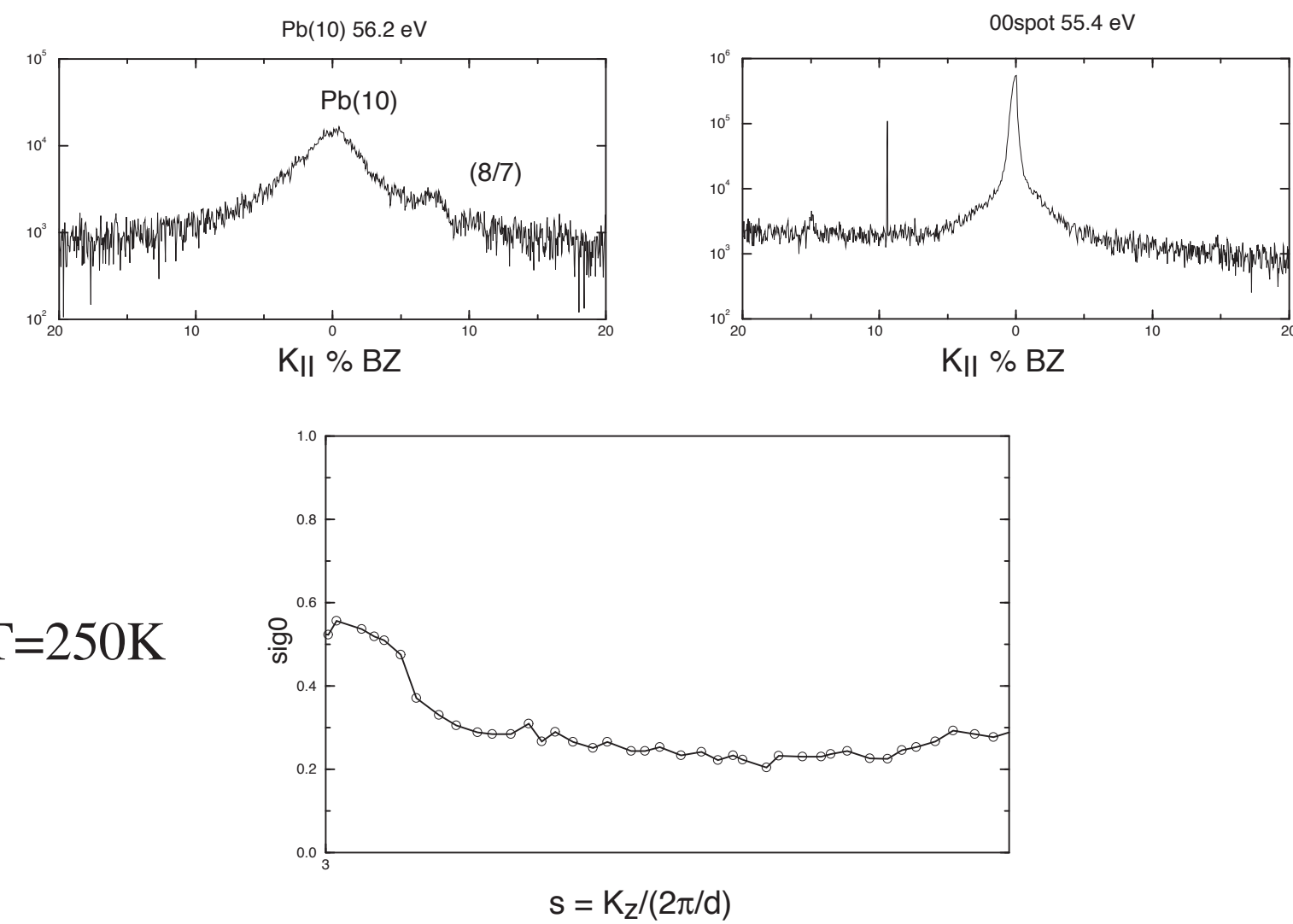
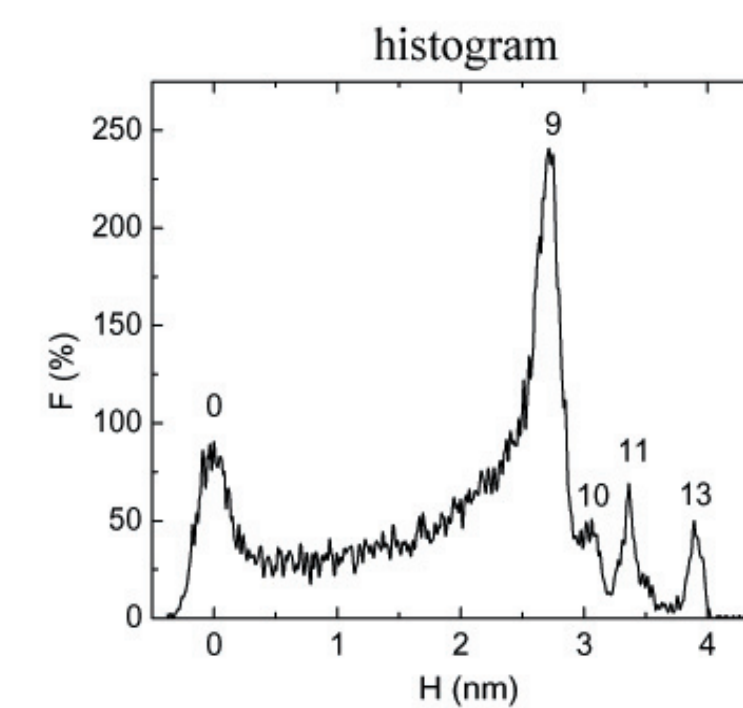
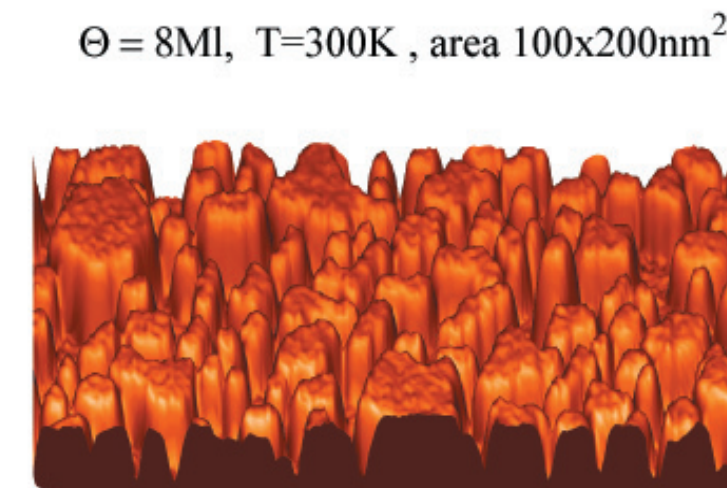


## How stable are the islands with thermal annealing?



No oscillations are observed in the  $g(s)$  curve ☐  
because the islands become larger in size and height.



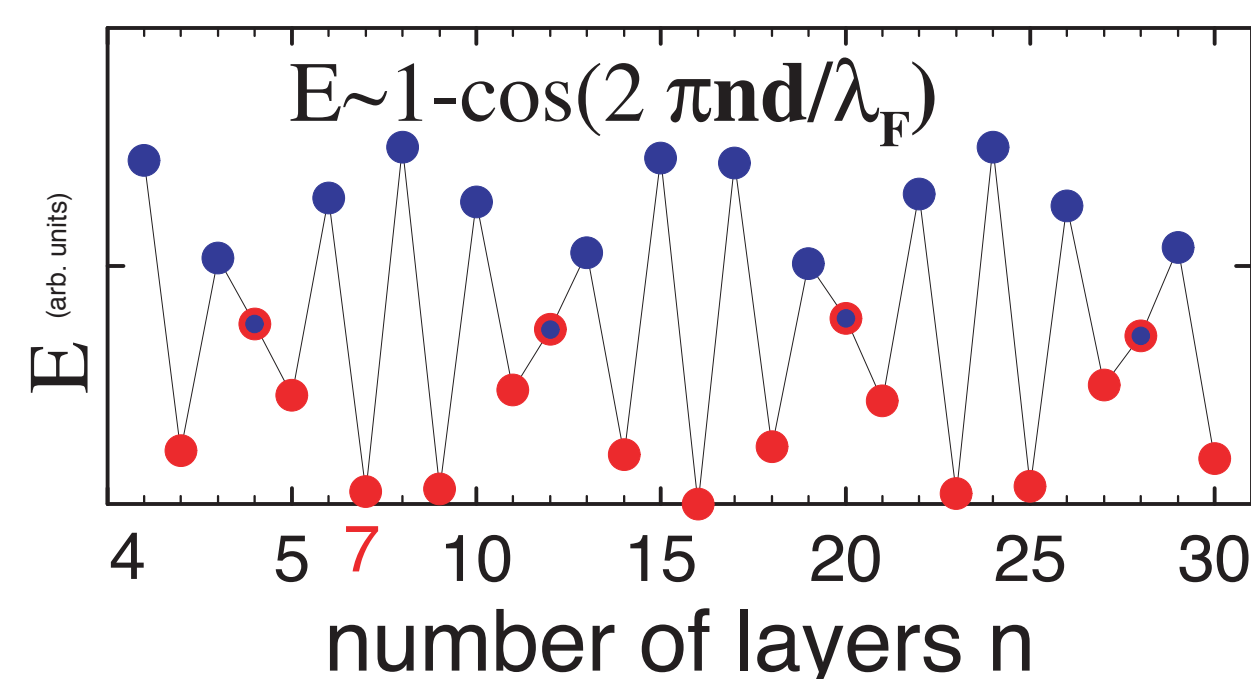
The island height increases ☐  
by bilayer increments.

## What is the origin of this unusual growth mode?

Island height  $7d$  is first multiple of Fermi wavelength.  
Growth with  $2d$  steps is explained by QSE, too.

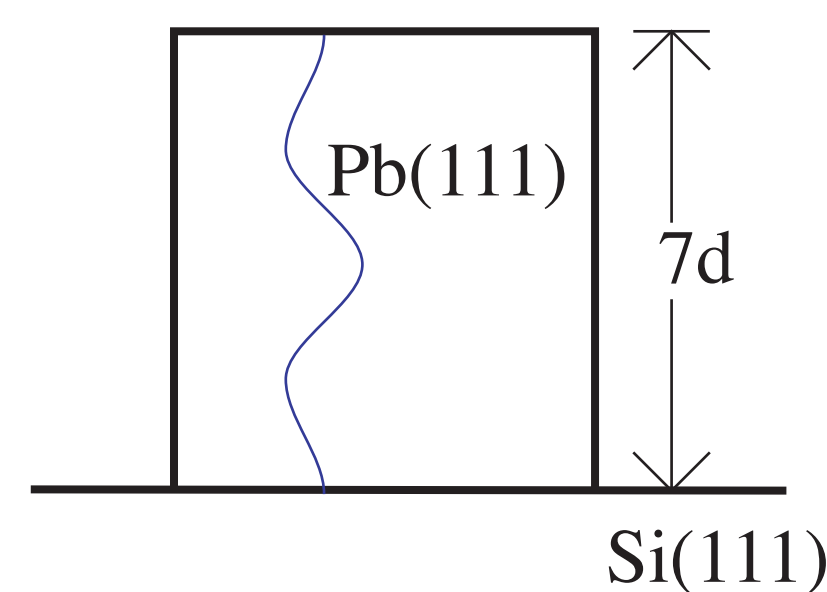
n	4	5	6	7	8	9	10
$2nd/\lambda_F$	6.25	7.81	9.38	10.94	12.50	14.07	15.62

$d=2.86\text{\AA}$   $\lambda_F=3.66\text{\AA}$  [A.Crottini et.al. PRL 79,8(1997)]



Minima are observed at 5, 7, 9,...layers

## QSE: Quantum Size Effects



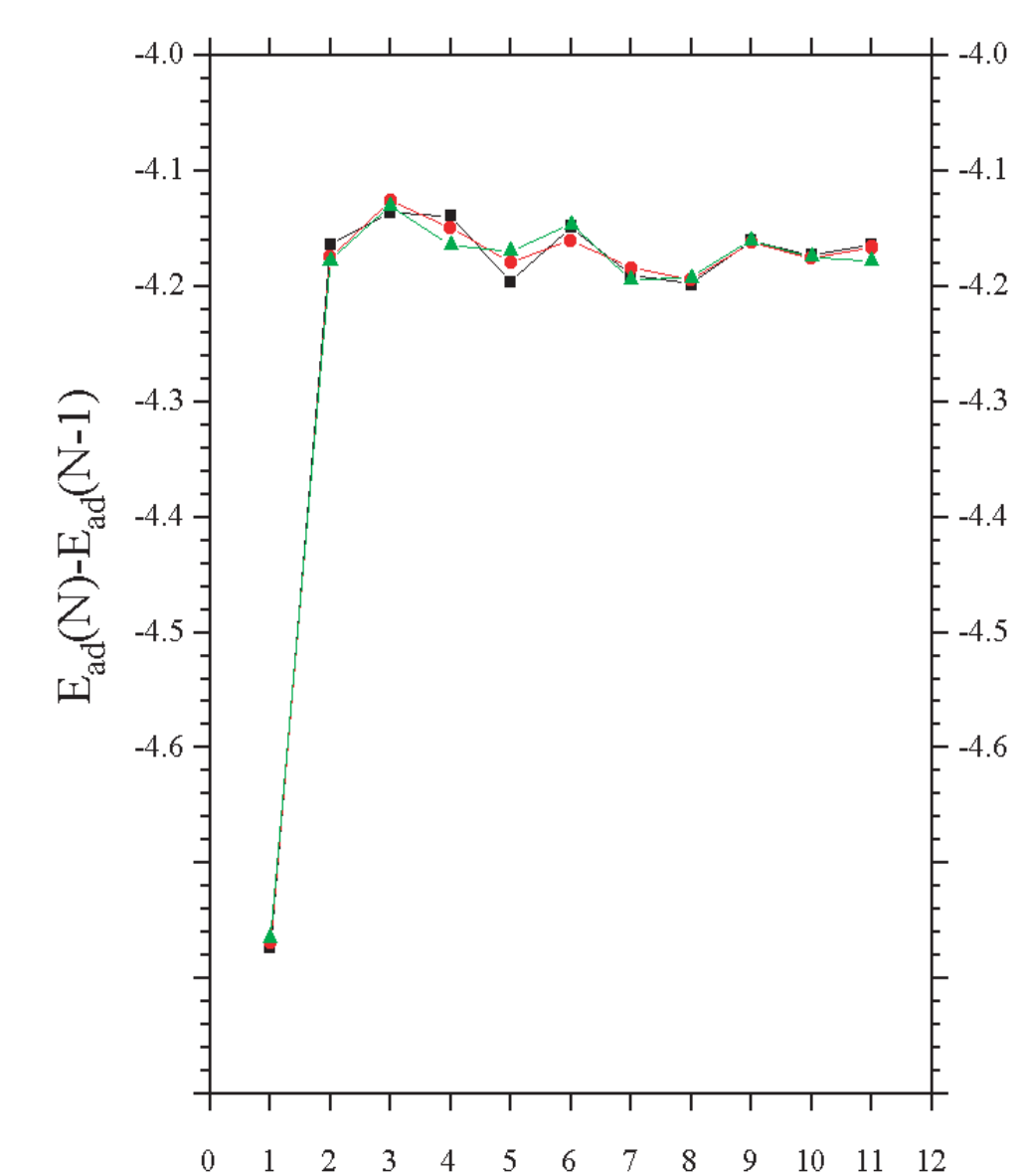
The electrons confined in the islands have "bulk" Fermi-wavelength  $\lambda_F$ . At what thickness the electron wavefunction forms stading waves? ☐  
☐

$$2nd / \lambda_F = \text{integer}$$

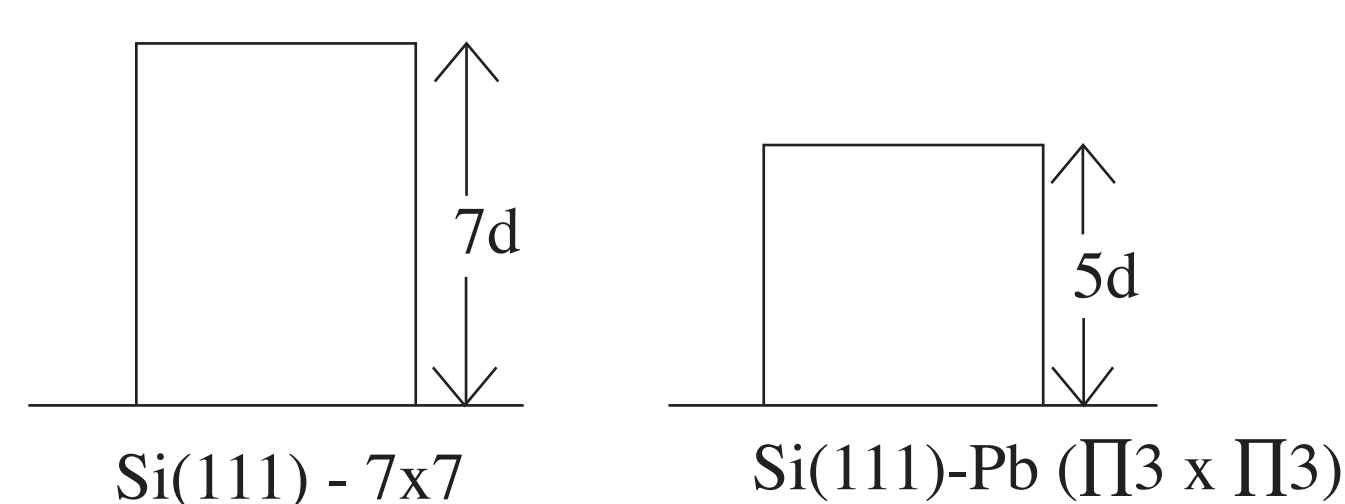
First Principles calculation of the energy minima of Pb on Si(11)-(1x1)

Minima are observed at  $h = 1, 5, 7, 10$

Pb layer growth on Si(111) (1x1) (Academia Sinica Taiwan)



## What is the role of the metal/semiconductor interface?

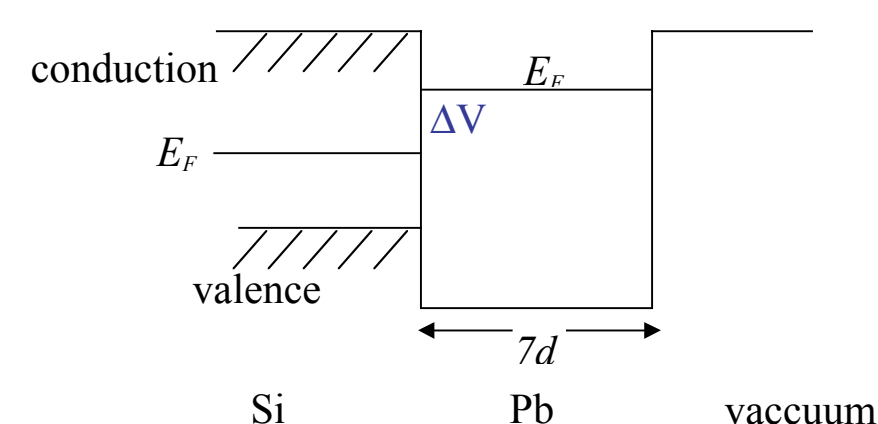


$$\mathcal{E} = \mathcal{E}_{\text{confinement}} + \mathcal{E}_{\text{charge transfer}}$$

Charge is transferred at the metal/semiconductor interface

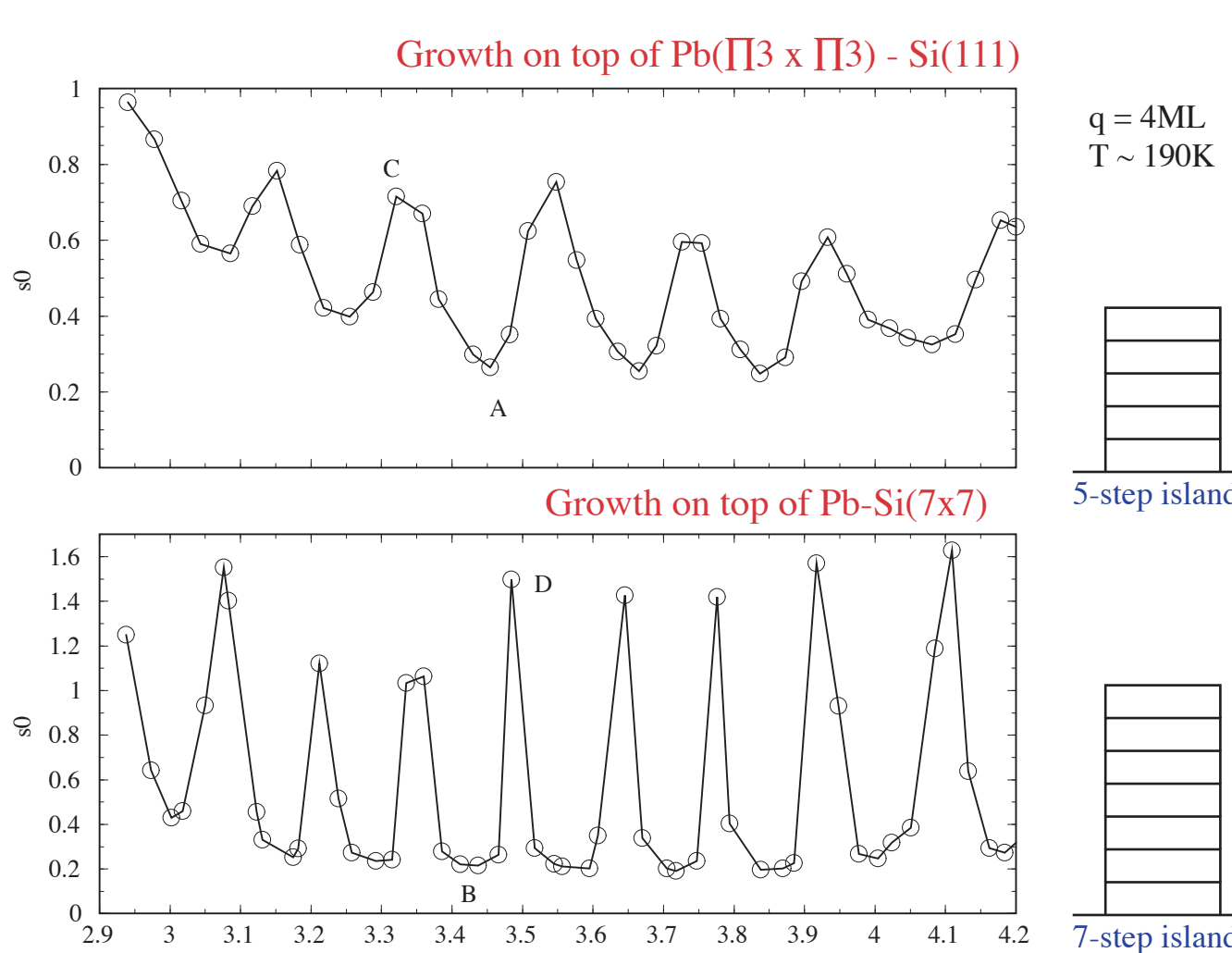
$$\mathcal{E}_{\text{cha}} = \frac{1}{2} q \Delta V^2$$

( $\Delta V$  = difference between the Fermi level in the semiconductor and the metal.) Zhang et. al PRL 80 5381 (1998).



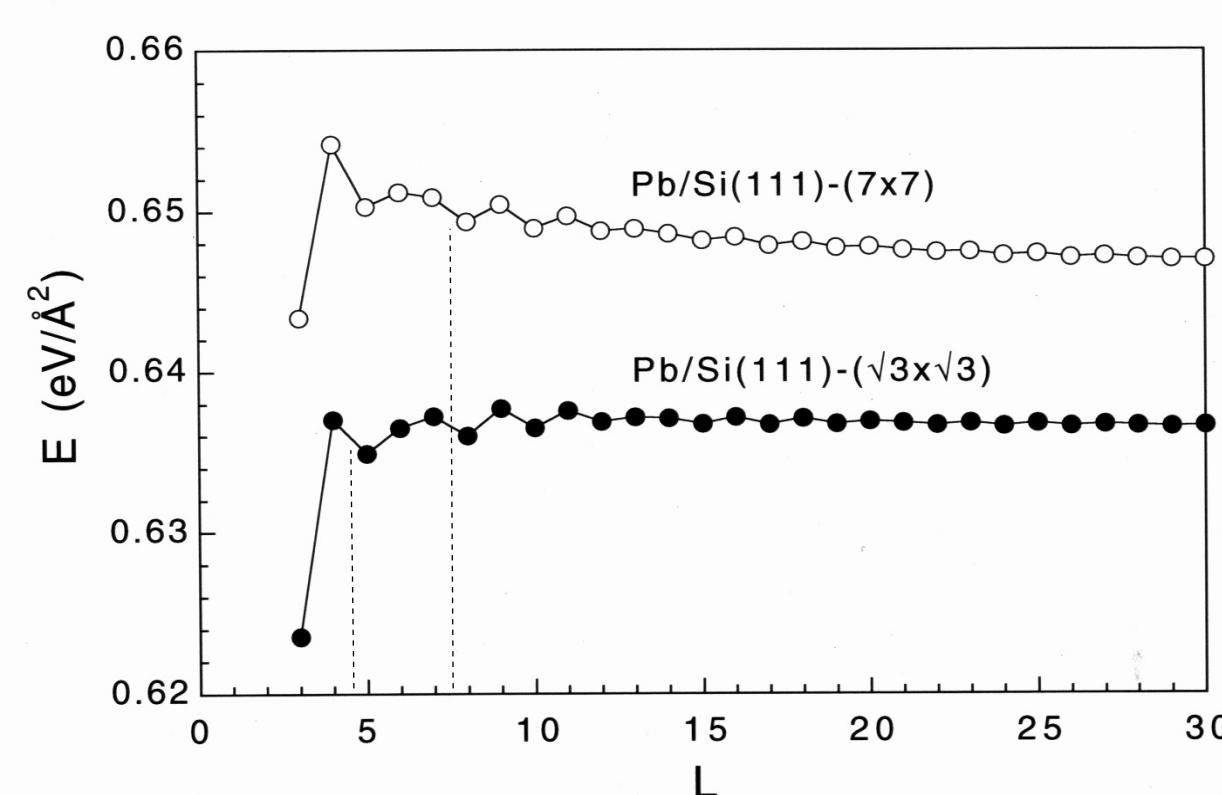
$\Delta V = 0.50 \text{ eV}$  (on Pb/Si-(7x7))  
 $\Delta V = 0.73 \text{ eV}$  (on Pb/Si-Pb( $\sqrt{3} \times \sqrt{3}$ ))

Larger contribution on  $\sqrt{3} \times \sqrt{3}$  than 7x7.  
Photoemission Weitering et al PRB 45 9126 (1992).



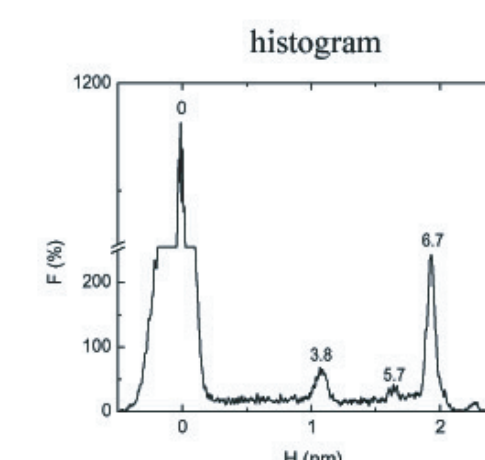
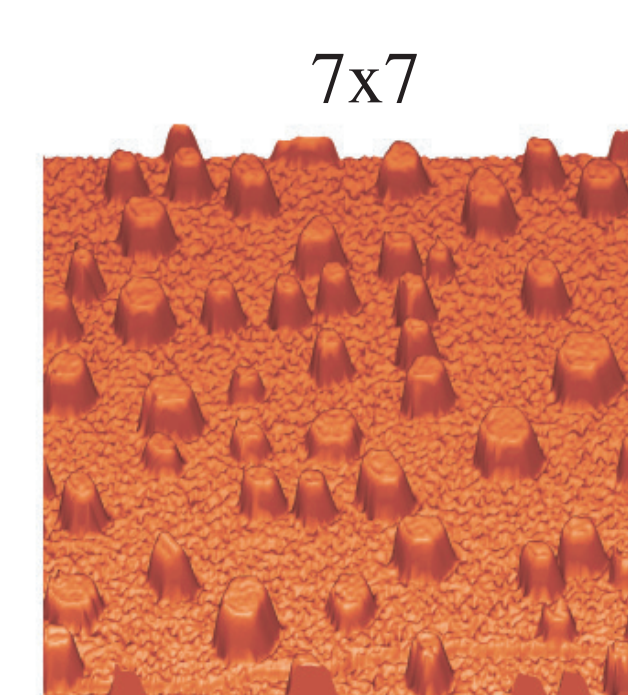
V. Yeh et. al; PRL 85 5158(2000)

Mostly 5-step islands are observed on the  $\sqrt{3} \times \sqrt{3}$  and mostly 7-step islands are observed on the (7x7).

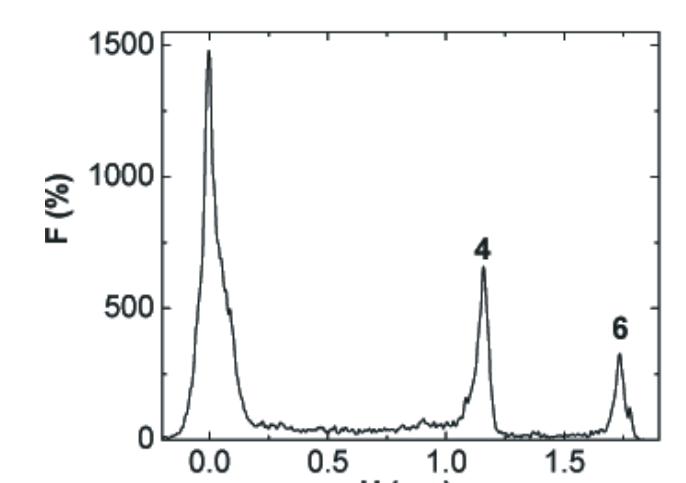
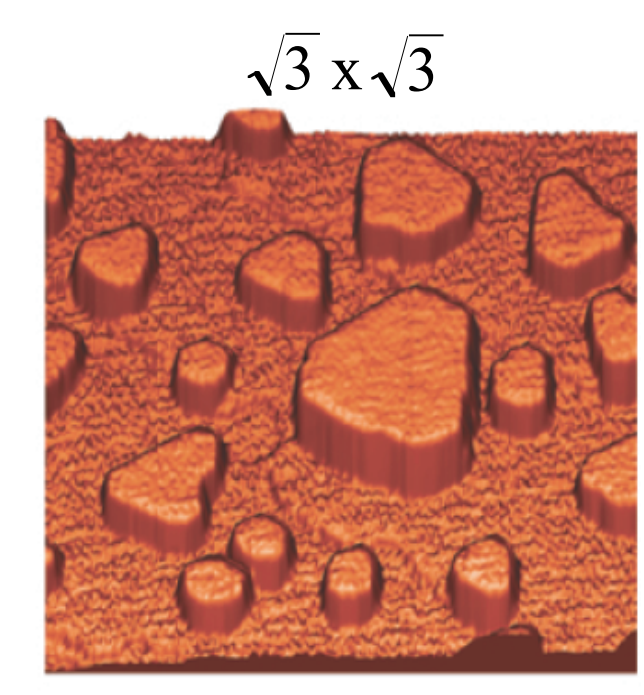


E vs L for the two different interfaces showing that  $L=5$  is a lower minimum on the  $\sqrt{3} \times \sqrt{3}$  than  $L=7$  on the (7x7) phase. ☐

200x200nm



$\theta=3.3ML$



$\theta=3.3ML$

$T=192K$

## Conclusions☐

I. Uniform height islands can be grown on Pb/Si(111) at low temperatures.

II. The  $(T, \theta)$  phase diagram can be used as a guide to select the island height.

III. The island height can be also controlled by selecting the interface.

IV. This unusual growth morphology confirms the role of QSE and charge transfer at the metal/semiconductor interface.